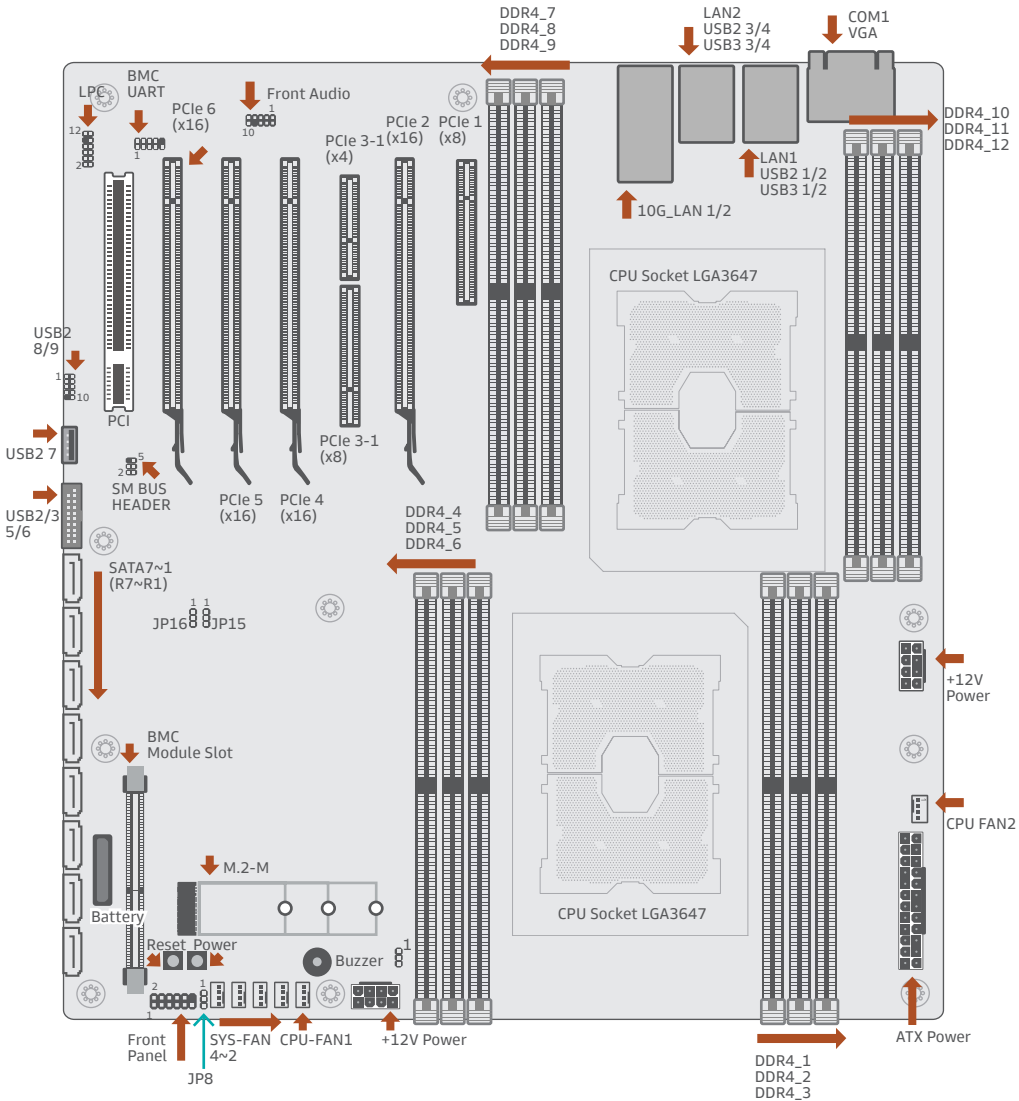
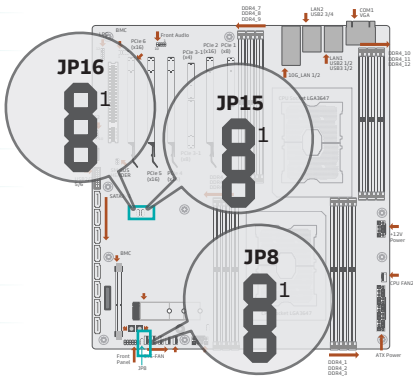


Board Layout and Jumpers



Note: These components are optional and may not be present on the actual board.

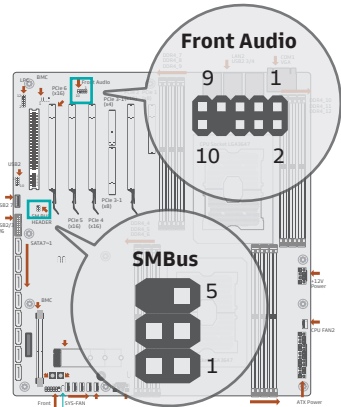
Clear CMOS Data	JP15
Normal (default)	1-2 On
Clear RTC Registers	2-3 On
ME Firmware Update	JP16
Normal (default)	1-2 On
ME Force Update	2-3 On
FLASH SECURITY OVERRIDE	JP8
DISABLE (DEFAULT)	1-2 On
ENABLE	2-3 On



Pin Assignment

Front Audio (Pitch 2.0mm)

Pin	Assignment	Pin	Assignment
1	Mic2-L	2	GND
3	Mic2-R	4	N.C.
5	Line2-R	6	Mic2-JD
7	GND	8	--
9	Line2-L	10	Line2-JD

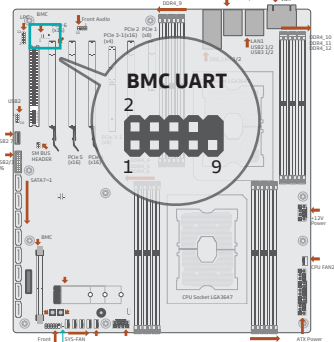


SMBus

Pin	Assignment	Pin	Assignment
1	3V3_AUX	2	GND
3	SMB_HOST_STBY_LVC3_SCL	4	SMB_HOST_STBY_LVC3_SDA
5	PU_PCH_TLS_ENABLE_STRAP	6	---

BMC UART

Pin	Assignment	Pin	Assignment
1	MDCD1-	2	MRD1
3	MTD1	4	MDTR1-
5	GND	6	MDSR1-
7	MRTS1-	8	MCTS1-
9	MRI1-	10	---

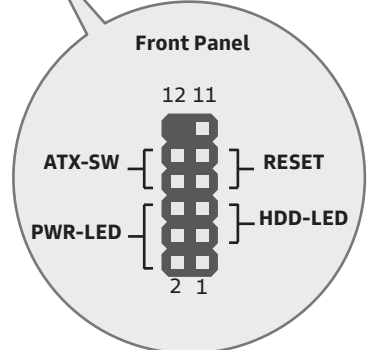
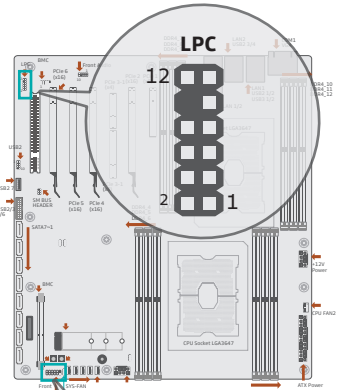


LPC (for Debug Port 80)

Pin	Assignment	Pin	Assignment
1	CLK	2	LAD1
3	RST#	4	LAD0
5	Frame#	6	3V3
7	LAD3	8	GND
9	LAD2	10	---
11	SERIRQ	12	N.C.

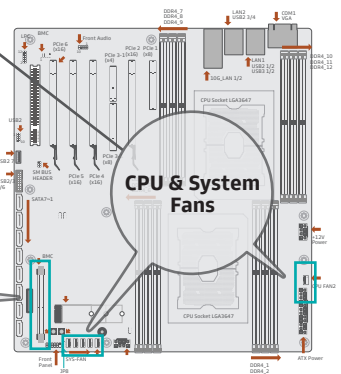
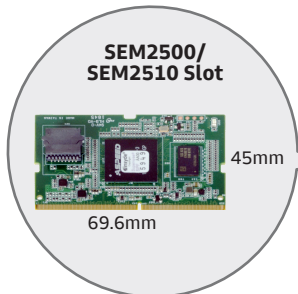
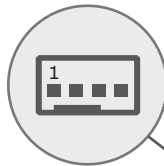
Front Panel

Pin	Assignment	Pin	Assignment
1	N.C.	2	LED Power
3	HDD Power	4	LED Power
5	Signal	6	Signal
7	Ground	8	Ground
9	Signal	10	Signal
11	N.C.	12	---



CPU and System Fans

Pin	Assignment
1	GND
2	+12V
3	Sense
4	Speed Control

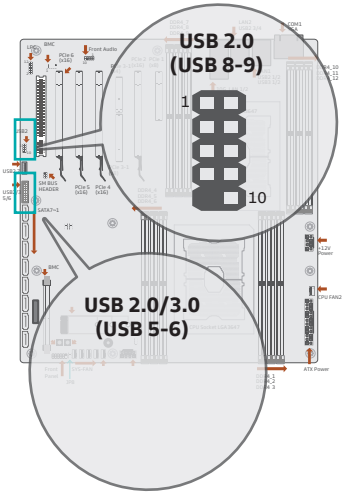


USB 2.0 (USB 8-9)

Pin	Assignment	Pin	Assignment
1	VCC	2	VCC
3	USB8_D-	4	USB9_D-
5	USB8_D+	6	USB9_D+
7	GND	8	GND
9	KEY	10	N.C.

USB 3.1 (USB 5-6)

Pin	Assignment	Pin	Assignment
1	PWR	11	D2+
2	SSRX1-	12	D2-
3	SSRX1+	13	GND
4	GND	14	SSTX2+
5	SSTX1-	15	SSTX2-
6	SSTX1+	16	GND
7	GND	17	SSRX2+
8	D1-	18	SSRX2-
9	D2+	19	PWR
10	N.C.	20	---



Note: The optional DDR4 SO-DIMM socket for the SEM2500/SEM2510 module supports additional IPMI management, BMC, and VGA output.